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## » Key

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IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

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IEEE STD IEEE Standard

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- ☐ 1. **DC-to-15- and DC-to-30-GHz CMOS distributed transimpedance amplifier:**  
Ren-Chieh Liu; Huei Wang;  
[Radio Frequency Integrated Circuits \(RFIC\) Symposium, 2004. Digest of Paper 6-8 June 2004 Page\(s\):535 - 538](#)  
Digital Object Identifier 10.1109/RFIC.2004.1320676  
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- ☐ 2. **1-Gb/s 80-dB/spl Omega/ fully differential CMOS transimpedance amplifie oxide technology for optical interconnects**  
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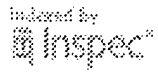
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- > EDP:
- > F&S:
- > Planning
- > Action
- > L1: (20) management and control and
- > L2: (20) local sector activities
- > School
- > Social
- > Services
- > Support FC
- > TEC
- > Training
- > Trade

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US Patent & Trademark Office												
Patent Application No. 10/123,456												
Inventor: John Doe												
Title: A Method for Reducing Power Consumption in a Wireless Communication System												
Abstract: A method for reducing power consumption in a wireless communication system. The method includes receiving a signal from a base station, and determining a power level for the signal. The power level is determined based on a received signal strength indicator (RSSI) value. The power level is then used to control the power of the signal. The power level is also used to control the power of the signal. The power level is also used to control the power of the signal.												
Claims:												
1. A method for reducing power consumption in a wireless communication system, comprising:												
receiving a signal from a base station;												
determining a power level for the signal based on a received signal strength indicator (RSSI) value;												
controlling the power of the signal based on the power level;												
controlling the power of the signal based on the power level.												
1	Document ID	Issue Date	Pages	Title	Current OR	Current Ref	Retrieval Class	Inventor	S	C	P	Ind
1	US 2003/024624 A1	10/12/04	17	Method for reducing a power consumption in a wireless communication system	350/300			Gee, Michael J.	P	C	P	US 2003/024624 A1
2	US 2003/014943 A1	08/28/03	19	Automatic gain control circuit with high frequency and monotonically controlled offset voltage	360/390			Frederick, John S. et al.	P	C	P	US 2003/014943 A1
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4	US 6538937 B2	03/03/05	10	Automatic gain control circuit with high frequency and monotonically controlled offset voltage	360/390	350/134, 350/154, 350/198, 350/204, 350/296		Frederick, John S. et al.	P	C	P	US 6538937 B2
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